

TITLE OF THE INVENTION

ENGINE CONTROL DEVICE

BACKGROUND OF THE INVENTION

This application claims the priority of Japanese patent document 11-228444, filed 12 August 1999, the disclosure of which is expressly incorporated by reference herein.

The present invention relates to an engine control device, and especially relates to an engine control device for screening an integrated circuit [by a status that] implemented in a circuit board.

Conventionally in a semiconductor integrated circuit for an engine control device of a vehicle, a chip which is cut [our] out from a wafer is screened [after having been] in addition to being protected by a package[,]; and after [the] any initial failure in the form of [that is] a latent defect is removed from the integrated circuit, the chip is [implemented in] mounted on a circuit board.

[In the] Recently, in semiconductor integrated [circuit of late years,] circuits the integrated circuit is mounted on a circuit board in bare [by bear] chip [status] form (without using the package), in order to reduce mounting size or area, to

improve electrical characteristics, and to achieve reduction of manufacturing cost.

In this case, it is not [effective as] efficient (a lot of time is needed) [,] to individually screen [the] each integrated circuit which is in [the bear] bare chip form [status] before mounting it on the circuit board, and it is expected to screen the integrated circuit after mounting it.

In the conventional equipment [which screens the] for screening an integrated circuit [of the bear] in bare chip form [status] on [the] a circuit board, a voltage regulator is [povided] provided in the circuit board. When [, when] the voltage [change over] changeover signal is [output] generated by a communication signal from an outside communications means, the voltage occurring in the resistor is amplified to [be] a high voltage (burn-in voltage) by [an] amplifier circuitry, and [the] screening is performed by adding [said] such high voltage in [said] the integrated circuit. (For example, Japanese Patent Laid-open No. 09-304481 and 10-009041 are referred to).

[By the way, in] In Japanese Patent Laid-open No. 09-304481 bulletin, it is disclosed to screen by adding a burn-in voltage on the integrated circuit after mounting it, wherein the conventional equipment needs a voltage supervisory circuit and a communications means other than a voltage regulator having a

resistor, an amplifier, etc. [and] Thus, the constitution thereof becomes [to be] complicated, and [any particular care-and-] no attention is [not done relating] given to reduction of manufacturing cost by [diminution of material] reducing the number of [items] components and the number of production [process] steps as a [characteritic] characteristic feature, [in a case] to mount the chip on the circuit board [by the bear] in bare chip form. [status.]

Furthermore, in a technique disclosed in Japanese Patent Laid-open No. 10-009041 bulletin, although [any] no external communications means [from] external is [not] needed. Constitution [, constitution] of [said] the voltage regulator and said voltage supervisory circuit are the same. Moreover, Japanese Patent Laid-open No. 09-304481 bulletin does not disclose anything relating to the reduction of the manufacturing cost.

SUMMARY OF THE INVENTION

[The present invention is invented referring to such subject stated above, and an] Based on the above prior art, one object of the present invention is to [provided] provide an engine control device having a voltage adjustment [circuitry by] which includes screening of a semiconductor integrated circuit mounted on a circuit board in [bear] bare chip form [status] (so as to reduce a mounting area thereof). [,]

Another object of the invention is to improve electric characteristics of such an engine control device and reduce its manufacturing cost by [planning] providing a simple constitution.

In order to achieve the above-stated object, an engine control device [in] according to the present invention [is characterized by comprising] comprises an integrated circuit [basically] (such as an arithmetic processing unit etc.) for processing operation of control programs, and an output voltage adjustment circuitry to switch a rated voltage to screen said integrated circuit. [,] According to the invention, the [wherein said] output voltage adjustment circuit comprises plural resistors provided between an output side power line (or the [said] rated voltage) and [a gland side.] ground.

Furthermore, according to the [preswent] present invention [is characterized in that said] the engine control device adds a [higher] voltage [value] which is greater than that in normal [usual] operation on [said] the integrated circuit when screening said integrated circuit. The [, and said] output voltage adjustment circuitry is connected to [an other] another resistor connected to [said gland side] ground and a connecting point between [said] the plural resistors is connected to [said] the other resistor, and is connected to [said] the constant voltage

source circuitry which outputs [said] the rated voltage to [said] the integrated circuit.

The [As the] engine control device of [ob] the present invention constituted as stated above[,] comprises [the] output voltage adjustment circuitry to switch the rated voltage to screen the integrated circuit, [said] the output voltage adjustment circuitry being constituted with three resistors at least. Accordingly, all integrated circuits can be screened after being mounted, by a low cost constitution without [constituting a] complicated circuitry; [,] and it becomes possible to reduce manufacturing cost and to save time of screening.

Furthermore, in [a concrete aspect of] the engine control device [in] of the present invention [, is characterized in that] the other resistor is connected with [the gland side] ground by switching on/off.

Furthermore, [the concrete aspect of] in the engine control device [in] of the present invention the [, is characterized in that said] other resistor is provided [in external of] externally to said engine control device, or is connected to a connecting point between said plural resistors through a non-used terminal of said engine control device[,].

Furthermore, [a concrete aspect of] in the engine control device [in] of the present invention [, is characterized in that] the other resistor is [provided in] internal [of] to said engine control device, or switches on/off based on a serial communication signal.

Furthermore, [the concrete aspect of] in the engine control device [in] of the present invention, the [is characterized in that said] arithmetic processing unit has a [strage] storage device to be capable of rewriting said control program by said serial communication signal so as to switch on/off, or [said] the arithmetic processing unit has a control program to switch over said rated voltage by said serial communication signal so as to switch on/off.

In the engine control device of the present invention constituted as above, as the program is rewritten with the serial communication signal or is constituted to be switched, all integrated [circuit] can be screened after being mounted [can be screened in a case any non-used] even when no unused terminal is [not] provided.

Furthermore, the control device [in] according to the present invention [is characterized by comprising said control device] comprises an integrated circuit such as an arithmetic processing unit etc. for processing operation of control

programs, an output voltage adjustment means to switch a rated voltage to screen [said] integrated circuit, and a check means to examine [characteristic] characteristics of said arithmetic processing unit and said integrated circuit.

Other objects, advantages and novel features of the present invention will become apparent from the following detailed description of the invention when considered in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a block diagram of [the] an engine control device according to a [as the] first embodiment [configuration] of the present invention[.];

Figure 2 is a block diagram of [the] an engine control device according to a [as the] second embodiment [configuration] of the present invention[.];

Figure 3 is a block diagram of [the] an engine control device according to a [as the] third embodiment [configuration] of the present invention[.]; and

Figure 4` is a timing chart which explains [operating] operation of the engine control device shown in [figure] Figure 3.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

One embodiment [configuration] of the engine control device in the present invention will be explained in detail using figures as follows.

Figure 1 is a block diagram to show a first embodiment [configuration] of the engine control device.

An engine control device 107 includes [is constituted with] a constant voltage source circuitry 100, an arithmetic processing unit (CPU) 111, and an integrated circuit (IC) 112, 113 etc. such as the CPU etc. and the CPU 111 and the integrated circuit 112, 113 are mounted on a circuit board in [bear] bare chip form. [status.]

[Furthermore, the] The engine control device 107 receives [takes in] a signal from a crank angle sensor [(illustration is omitted),] (not shown), and processes based on detecting signals, so as to output a driving signal to fuel injection valves [(illustration is omitted)] (not shown) and [outputs] an ignition driving signal to a spark plug [(illustration is omitted),] (not

shown). The [and the] engine control device 107 [is constructed with] includes an input circuit [(illustration is omitted)], an AD [conversion] converter part [(illustration is omitted)], and a [storadge] storage device POM [(illustration not omitted).] (not shown). The input circuit takes in an input signal (for example, signals from a coolant temperature sensor, a crank angle sensor, and an air fuel ratio sensor etc.), [and] removes noise [component from the input signal so as to supplyes] components, and supplies the input signal to an AD [conversion] converter part, which [. The AD conversion part] converts it [the input signal] and [it is output into] outputs it to the CPU 111. The CPU 111 takes in the signal converted by the AD conversion and executes a predetermined control program stored in the ROM, [thereby has a function] and functions to execute control of the device. Further, the operation result and the AD conversion result are stored temporarily in the RAM, and the operation result [of] is output as a control output signal through the output circuit [so as] to be used to control the fuel injection valves.

[Furthermore, the] The engine control device 107 is connected with a check equipment (a screening equipment) 108 through a connector in order to screen the CPU 111 and the integrated circuits 112, 113. Thereafter, [and after having screened the integrated circuits 112, 113,] characteristics of the CPU 111 and the integrated circuits 112, 113 are checked for

failures [so as to distinguish good or bad] thereof. After they are [, then, after being] separated from the check instrument 108, only a good device is [adopted] provided to an actual machine as the engine control device 107.

The check instrument 108 [is capable of supplying] supplies the battery voltage 304 to a constant voltage source circuitry 100 of the engine control device 107 through an input side power line 109, thereby connecting the constant voltage source circuit 100 [is connected] with an output voltage adjustment circuitry 150, [and the output voltage adjustment circuitry 150] which switches the rated voltage so as to screen the integrated circuits 112, 113.

The constant voltage source circuitry 100 is a variable output type one and [constructed with] includes a current source 114, a reference voltage source 104, a start up circuitry 115, an error amplifier 116, and a transistor 117. It [, and] outputs a constant rated voltage (for example, 5V) to the CPU 111 and the integrated circuits 112,113 through the output side power line 110.

The output voltage adjustment circuitry 150 switches [said] the rated voltage as stated above, so as to screen the CPU 111 and the integrated circuits 112, 113. It includes [and is constructed with] resistors 101,102,106 and an electric switch

105, the resistors [and the resistor] 101 and [the resistor] 102 being [are] connected in series between the output side power line 110 of the rated voltage and [the gland side.] ground. On the other hand, the other resistor 106, separate [to be different] from the resistors 101, 102, is provided in an other output voltage adjustment circuitry 150a inside of the check equipment 108[,]; one end thereof is connected to [the gland side] ground through the electric switch 105, and another end thereof is connected with a connecting point 103 between the [resistors] resistors 101, 102. It [, and it] is connected with the resistor 102 in parallel by turning the electric switch 105 on.

In addition, the other resistor 106 is connected with the connecting point 103 between the resistors 101, 102 through a non-used terminal of the engine control device 107, and is simultaneously connected with a reference voltage source 104 of the constant voltage source circuitry 100.

The screening for the CPU 111 and the integrated circuits 112, 113 is performed as follows.

At first, the battery voltage 304 is supplied to the engine control device 107 by the check equipment 108 through the input side power line 109[,]; and the constant voltage source circuitry 100 [supplies] supplies a source voltage of the usual constant

rated voltage (for example, 5V) to the CPU 111 and the integrated circuits 112, 113 through output side power line 110. As a result, [thereby] the CPU 111 and the integrated circuits 112, 113 start to [operat.] operate.

[This] The voltage value [of] at the connecting point 103 in normal operation [a usual time] (intermediate voltage value) is determined according to a ratio of the [resistor] resistors 101 and [the resistor] 102 connected in series, and the constant rated voltage is maintained by comparing the intermediate voltage value with a voltage value of the reference voltage source 104 with the error amplifier 116.

During [In the other, when] screening, the voltage [value of the] at connecting point 103 (intermediate voltage value) is determined to be switched according to a ratio of the resistors 102 and 106 connected in parallel and the resistor 101 connected therewith in series, by turning the electric switch 105 of the output voltage adjustment circuitry 150, and a voltage value which is higher than that in [the usual time] normal operation is supplied to the CPU 111 and the integrated circuit 112, 113 through the output side power line 110 so as to screen them.

After finishing the screening, the electric switch 105 is turned off, and the characteristic of the device is tested with the check equipment 108 [after] at a predetermined time [from the

finishing, then the] thereafter, any initial failures (being a potential defect of the CPU 111 and the integrated circuit 112, 113) are removed by checking the function of the engine control device 107.

Figure 2 is a block diagram which shows a second embodiment [configuration] of the engine control device according to the invention. [, and as figure] As Figure 2 shows the same construction as [figure] Figure 1, other than [besides] a CPU 200 and an output voltage adjustment circuitry 250, [and] only the CPU 200 and the output voltage adjustment circuitry 250 will be explained in detail.

The [Said] CPU 200 [builds in] includes a ROM (a flash ROM) which is capable of rewriting the control program and controls the intermediate voltage of the connecting point 103 based on [by] the rewritten control program. The output voltage adjustment circuitry 250 is used to switch the rated voltage in order to screen the integrated circuit 112,113 and [is constructed with the] includes resistors 101, 102, 201 and the transistor 204. The [, and the] resistor 101 and the resistor 102 are connected in series between the output side power line 110 of the rated voltage and ground. On the other hand, the other resistor 201, which is separate [to be different] from the resistors 101, 102, is provided inside of the engine control device 107; one end thereof is connected with ground via the

transistor 204 and another end thereof is connected with the connecting point 103 between the resistors 101, 102. It [, and it] is connected in parallel with the resistor 102 [in parallel] by turning the electric switch 204 on.

In addition, a serial communication means 203 is arranged external to [in an outside of] the engine control device 107. The [, an serial communication means 203 is arranged, and the] CPU 200 is switched into a write mode by [being transmitted] the output signal and the control program from the serial communication means 203, and simultaneously the control program of the flash ROM in the CPU 200 is rewritten.

In normal operation, by using the serial communication means 203, a program to [fix] set the output [of] at a port 202 of the CPU 200 [to be] at a low level is written in the flash ROM, and the transistor 204 is turned off.

The intermediate voltage value is determined [according to a] by the ratio of the resistors 101 and 102 connected in series.

On the other hand, when screening, a program to [fix] set the output of the port 202 of the CPU 200 [to be] at a high level is written in the flash to be rewritten by the serial communication means 203. The [, and the] serial communication means 203 turns on the transistor 204 and determines the voltage

[value of] at the connecting point 103 (intermediate voltage value) so as to switch it based on a ratio of the resistors 102 and 106 connected in parallel and the resistor 101 connected in series therewith. A [, and the] voltage [value] which is higher than that in [the usual time] normal operation is supplied [on] to the CPU 200 and the integrated circuit 112, 113 through the output side power line 110 so as to screen thereof.

After [finishing the] completion of screening, the program to [fix] set the output [of] at the port 202 to [be] a low level again is rewritten on the flash ROM by a serial communication means 203. As a result, [thereby] the transistor 204 is turned off and characteristics of the device [is] are checked [with] by the check equipment 108 [after] at a predetermined time thereafter. Initial [has passed, then the initial] failures [being] (a potential defect of the CPU 200 and the integrated circuit 112, 113) are then removed by checking the function of the engine control device 107.

Figure 3 is a block diagram which shows a third embodiment [configuration] of the engine control device according to the invention. As Figure [, and as figure] 3 shows the same construction as [figure] Figure 2 other than the [besides a] CPU 300, [and] only the CPU 300 will be explained in detail.

The CPU 300 [builds in] contains the control program to switch a program to [fix] set the output [of the] at port 309 to [be the] a low level, and a program to [fix] set the output [of the] at port 309 to [be the] a high level mutually, so as to control the intermediate voltage of the connecting point 103.

The output voltage adjustment circuitry 303 is constituted in the same way as output voltage adjustment circuitry 250 shown in [figure] Figure 2. It includes [, has] the [resistor] resistors 101, 102 and 201 and the transistor 204, and switches the rated voltage in order to screen the integrated circuits 112, 113.

The resistors 101 and 102 [is] are connected in series between the output side power line 109b of the rated voltage and [the gland side.] ground. The one end of the other resistor 201 is connected to [the gland side] ground through the transistor 204, and the other end of the resistor 201 is connected with the connecting point 103 between the [resistors] resistors 101, 102 and is connected with the resistor 102 in parallel by turning the transistor 204 on.

In [the usual time,] normal operation, a program to [fix] set the output of the port 309 of the CPU 300 to a [be the] low level is designated by the serial communication means 203 so as to turn the transistor 204 off. on the other hand, during [, and

when performing the screening, a program to [fix] set the output of the port 309 of the CPU 300 to [be the] a high level is designated by the serial communication means 203 so as to turn the transistor 204 on, and [the] a voltage value which is higher than [that] in [the usual time] normal operation is supplied to the CPU 300 and to the integrated circuits 112,113 through the output side power line 110 so as to screen them. After the screening is completed, the program to fix the output of the port 309 to be the low level is designated, the transistor 204 is turned off, and after the predetermined time has passed, the characteristic of the device is checked by the check equipment 108. Initial [, then the initial] failures [being a] (potential [defect] defects of the integrated circuits 112, 113) are removed by checking the function of the engine control device 107.

Figure 4 is a timing chart of operating of the engine control device shown by [figure] Figure 3.

At first, the battery voltage 304 is applied to engine control device 107 through the input side power line 109a by using the check equipment (a screening test device) 108, and the constant voltage source circuitry 100 [supplies] supplies a source voltage having the constant rated voltage (for example, 5V) in [the usual time] normal operation to the CPU 300 and the integrated circuit 112, 113 through the output side power line

109b so as to start to operate the CPU 300 and the integrated circuit 112,113.

[In the next,] Next, when a high voltage switching designation is transmitted to the communication line 301a from the serial communication means 203 of the check equipment 108, the CPU 300 receives the high voltage switching designation signal 305 through the serial receiver circuit 301, the high voltage switching designation signal 305 is output by the output voltage adjustment circuitry 303 so as to turn the transistor 204 on, and the output voltage value of the constant voltage source circuitry 100 is changed to [be the] a high voltage value (7V, for example) for [the] screening.

In addition, the CPU 300 transmits a [change over] changeover completion signal 307 to the communication line 302a through a serial transmitting circuit 302 and the signal 307 is returned to the check equipment 108.

Then, after having applied the high voltage in the predetermined time that is necessary for the screening, when the rated voltage [switch over] switchover designation is transmitted to the communication line 301a from the serial communication circuitry 203 of the check equipment 108, the CPU 300 receives the rated voltage [change over] changeover designation signal 306 through the serial receiver circuit 301. The [, the] rated

voltage [change over] changeover designation signal 306 is output to the output voltage adjustment circuitry 303, the transistor 204 is turned off, and the output voltage value of the constant voltage source circuitry 100 is changed to [be] the rated voltage (5V, for example).

In addition, the CPU 300 transmits the [change over] changeover completion signal 308 to the communication line 302a through the serial transmitting circuit 302, and the signal 308 is returned to the check equipment 108.

Then, after the predetermined time is passed following completion [from the finishing] of the screening, the characteristic of the device is tested by the check equipment 108, and the initial failures [being] (potential defects of the integrated circuits 112, 113) are judged by checking the function of the engine control device 107.

As stated above, the embodiment configuration in the present invention has the following function based on the [constitution] construction mentioned above.

The engine control device 107 according to [in] the first embodiment [configuration] of the present invention comprises the CPU 111 to operate the program, integrated circuits 112, 113, the ROM to store the program, the constant voltage source circuitry

100 to output the rated voltage to the integrated circuits 112, 113 etc. and the output voltage adjustment circuitry 150 to switch the rated voltage in order to screen the integrated circuits 112, 113 mounted on the circuit board.

The output voltage adjustment circuitry 150 is connected to other output voltage adjustment circuitry 150a so as to connect the resistors 101, 102, 106 with the electric switch 105. The [, the] resistors 101 and 102 are connected in series between the output side power line 110 of the rated voltage and [the gland sides, one] ground. One end of the other resistor 106 is connected to [the gland side] ground and other end thereof is connected to the connecting point 103 of the resistors 101, 102. Moreover, [and] the other resistor 106 is connected in parallel with the resistor 102 [in parallel] by turning the electric switch 105 on; [,] and the reference voltage source 104 of the constant voltage source circuitry 100 is connected with the connecting point 103 between the resistors 101, 102 through the non-used [terminal] terminal A of the engine control device 107. Therefore, the screening of all integrated circuits 112, 113 (after being [mount] mounted) can be performed [at a time] by changing the intermediate voltage of connecting point 103, without providing [the] complicated circuitry and the outside output signal means especially, thereby reducing [reduction of] the manufacturing cost and shortening of the time of the screening. [can be planned.]

Furthermore, in [In] the above embodiment, [configuration further,] the voltage value is changed for the screening, and the reduction of the power consumption can be planned by using the change of this voltage value for engine control. In the engine control device 107 in the second embodiment, [configuration,] the CPU 200 [builds in the] includes a ROM (flash ROM) in which the program stored therein can be [thereof is capable of being] rewritten. The [, the] program of the CPU 200 is written in by the serial communication means 203, [thereby] so that the transistor 204 of the output voltage adjustment circuitry 250 is switched on or off so as to change the intermediate voltage of the connecting point 103, and in a case of an engine control device having a [limited] limited terminal and not having a non-used terminal A, [the] screening of the integrated circuits 112, 113 after mounting can be performed.

Furthermore, in the engine control device 107 of the third embodiment, [configuration,] the CPU 300 [build in] includes the control program to mutually switch a program to [fix] set the output of the port 309 of the CPU 300 to [be in the] a low level or a [and a program to fix output of the port 309 to be in the] high level according to the output signal from the serial communication means 203. The [, the] programs of the CPU 300 are switched by the serial communication means 203, [thereby,] whereby, the transistor 204 of the output voltage adjustment

circuitry 303 is [swtched] switched on or off and the intermediate voltage of the connecting point 103 [can not] cannot be changed; [,] and if the engine control device does not have an unused [non-used] terminal A and the CPU does not have the flash ROM, [the] screening of the integrated circuit 112, 113 after mounting can still be performed.

Although the embodiment [configuration] of the present invention is explained in detail as above, the present invention is not limited to the above embodiments. Various [embodiment configuration, various] kinds of modification are possible in design without deviating from the present invention mentioned in the claims.

For example, in the first embodiment, [configuration,] the output voltage adjustment circuitry is divided into the engine control device and the check equipment; [,] however, this circuitry may be provided as one body in the engine control device.

In addition, the check equipment is not [limited to be used for only the] limited to use only for engine control. By [device, and by] providing the output voltage adjustment circuitry and the characteristic check circuit as the control device of the integrated circuit mounted as the bear chip status, the

[creening] screening of the integrated circuit and the function test can be performed.

As [being] is apparent from the above-mentioned description, the engine control device in the present invention can perform the screening of the semiconductor integrated circuit mounted as [the bear] a bare chip form [status] on the circuit board, by arranging the output voltage adjustment circuit, and reduction of the mounting area size and improvement of the electrical characteristic nature becomes possible and reduction of the manufacturing cost can be planned too by a simple constitution thereof.

The foregoing disclosure has been set forth merely to illustrate the invention and is not intended to be limiting. Since modifications of the disclosed embodiments incorporating the spirit and substance of the invention may occur to persons skilled in the art, the invention should be construed to include everything within the scope of the appended claims and equivalents thereof.